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Patentanmeldung Nr. Patent application No. Demande de brevet n°

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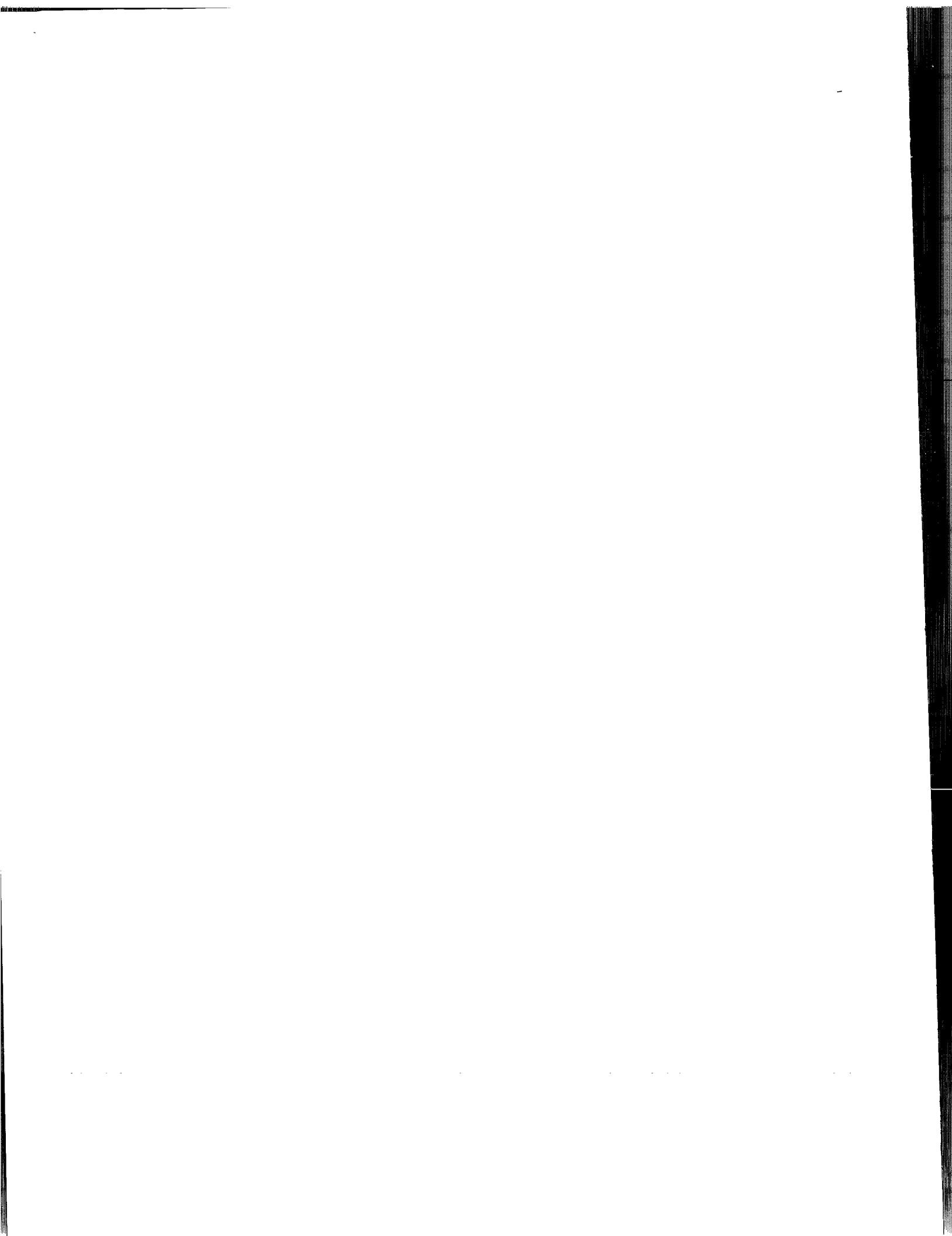
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Bezeichnung der Erfindung/Title of the invention/Titre de l'invention:

(Falls die Bezeichnung der Erfindung nicht angegeben ist, siehe Beschreibung.
If no title is shown please refer to the description.
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Switch mode power supply with output voltage equalizer

In Anspruch genommene Priorität(en) / Priority(ies) claimed /Priorité(s)
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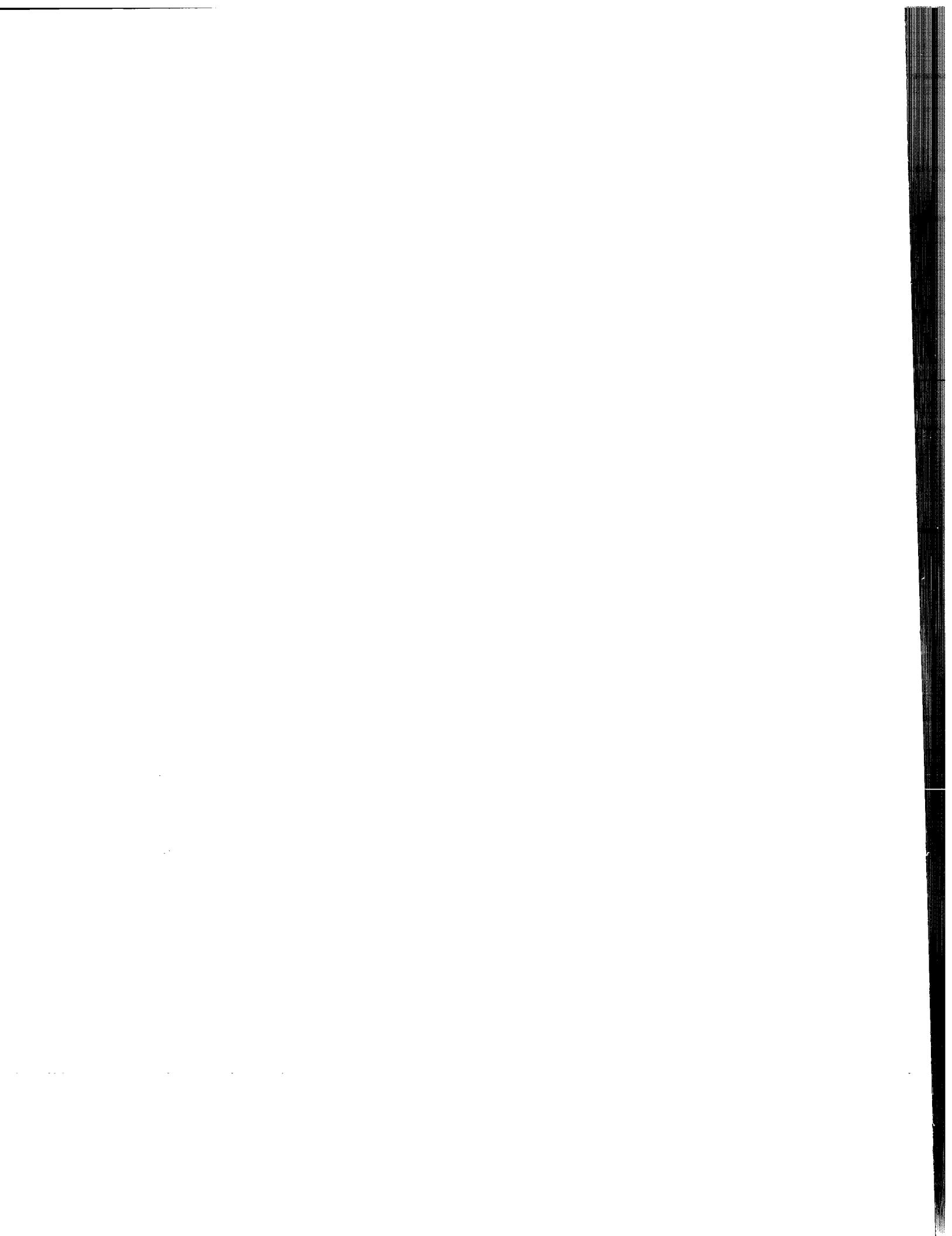
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Switch mode power supply with output voltage equalizer

The present invention relates to a switch mode power supply with two (inverted) voltage sources. Such power supplies are normally used for driving class D amplifiers. The invention also relates to a class D amplifier driven by such a power supply.

A half bridge based (so called single ended) Class D amplifier requires a 5 power supply having two inverted voltage sources, and normally a switch mode power supply is used. When such a power supply is connected to a class D amplifier, a phenomenon called the "pump effect" arises, meaning that for low audio frequencies, the class D amplifier transfers energy from one of the voltage sources to the other. This effect can be quite substantial, leading to impaired performance of the amplifier and can even lead to destruction 10 of the ICs in the Class D amplifier.

In order to overcome this problem, the capacitance value of the output capacitors can be set very large, thereby reducing the ripple to an acceptable value compared to the output voltage. However, the output capacitors, which typically are electrolytic capacitors, then become very large and expensive.

15 Alternatively, a dissipative power clamp can be arranged on each voltage source, thereby dissipating any excess of power that is transferred to the voltage source. The drawback is of course that this leads to reduced efficiency, thermal problems and additional bulky components.

20

It is an object of the present invention to overcome this problem, and to provide output voltage equalizing of a switch mode power supply.

According to the invention, this and other objects are achieved with a switch mode power supply comprising two voltage sources, the negative side of the first source 25 being connected to the positive side of the second source in a common point, and further comprising a voltage equalizer circuit including a coil having a first terminal connected to the common point, a first switch connected between a second terminal of the coil and the positive side of the first source, a second switch connected between said second terminal of the coil and the negative side of the second source, and control circuitry for determining when the

voltage of one of the voltage sources exceeds a predetermined threshold value, and, in response to such a determination, periodically operate the switch associated with said one voltage source ON and OFF, thereby redistributing energy from said one voltage source to the other voltage source.

5 The inventive voltage equalizer thus acts as a switch mode converter, redistributing energy from one voltage source to the other, thereby ensuring that the output voltages of both sources are essentially equal. Further, the voltage equalizer requires very few components, and is thus simple to implement. As the two switches only need to handle the excess of power they can be relatively small and cost efficient.

10 According to a preferred embodiment, the control circuitry comprises one control circuit for each switch, each circuit including a comparator connected in parallel with the corresponding voltage source for generating an activation signal for as long as the source output voltage exceeds the threshold value, and an oscillator connected to the gate of the switch and receiving said activation signal, for providing a periodic gate signal when said 15 activation signal is present. This design of the required control circuitry is very simple, further improving the cost efficiency of the inventive voltage equalizer.

20 The comparator is preferably a hysteresis comparator, adapted to inhibit the activation signal when the source output voltage falls below a second threshold value. By having different thresholds for activating and deactivating the periodic switching of the equalizer, a more stable process is achieved.

The switches are preferably MOSFET switches. An advantage with these switches is that they inherently provide a rectifying effect by means of the parasitic body diode of the MOSFET's, making an additional rectifying diode superfluous.

25 The switch mode power supply is advantageously used to drive a class D amplifier.

30 This and other aspects of the present invention will now be described in more detail, with reference to the appended drawings showing a currently preferred embodiment of the invention.

Fig. 1 is a schematic circuit diagram of a switch mode power supply according to the invention.

Fig. 2a is a diagram showing the output voltages of the power sources in figure 1.

Fig. 2b is a diagram showing the activation signal from the comparator and the gate signal generated by the oscillator in one of the control circuits in figure 1.

Fig. 2c is a diagram showing the current through the coil in figure 1.

5

The power supply in figure 1 has two inverted power sources in the form of two electrolytic capacitors (elcaps) 1, 2 connected in series, with the negative side of the upper elcap 1 and the positive side of the lower elcap 2 connected to a common point 3. In parallel to each source is connected a circuit comprising one side of a transformer winding 4, 10 5 and a diode 6, 7 in series, such that the two transformed windings 4 and 5 are connected to each other in the common point 3. In use, the transformer windings 4 and 5 will transfer energy via diodes 6 and 7 to a load, e.g. a class D amplifier (not shown). As an example, the output voltages of the voltage sources 1 and 2 can be +/-27 V.

According to the invention, the power supply in fig. 1 is further provided with 15 a coil 10, connected between the common point 3 and the first terminals of two switches 11 and 12, the second terminals of which are connected to the other sides of the elcaps 1 and 2. Each of the voltage sources 1, 2 is thus connected in parallel with a circuit comprising a switch 11, 12 and the coil 10 in series.

The switches 11, 12 can be transistor switches such as MOSFETs. The upper 20 switch 11 is preferably a p-channel MOSFET, with its drain connected to the coil 10 and its source connected to the positive side of the upper voltage source 1, while the lower switch 12 is preferably a n-channel MOSFET, with its source connected to the coil 3 and its drain connected to the negative side of the lower voltage source 2. With such a design, the drive of the MOSFETs is simplified (no floating drive is required).

Each switch 11, 12 is controlled by a control circuit 13 and 14, respectively, in the illustrated example each comprising a comparator 15, 16 and an oscillator 17, 18. The 25 comparator 15, 16 of each control circuit is connected in parallel with its respective voltage source 1, 2, and has an output signal, referred to as an activation signal 19, 20, connected to the oscillator 17, 18. The oscillator, which is voltage supplied by the voltage source 1, 2, has its output 21, 22 connected to the gate of the respective switch 11, 12. The comparators are 30 preferably of hysteresis type, with a given reference voltage and a given hysteresis. In the given example, the reference voltage can be 29 V, with a hysteresis of 1 V.

The function of the circuit in figure 1 will now be described with reference to figure 2a-c. In figure 2a, the voltages of the two elcaps are shown, at a point t_1 in time, the

output voltage v_2 of the lower elcap exceeds the output voltage v_1 of the upper elcap more than a predetermined threshold value, $v_{\text{thresh,ON}}$, in the example given above at 29 V. This is detected by the comparator in the lower control circuit, which generates an activation signal to the oscillator. The oscillation signal is shown in figure 2b.

5 Each time the oscillation signal is high, the lower MOSFET is switched ON, and the coil is connected over the lower elcap. As shown in figure 2c, the current (and thus the flux) in the coil increases with time at a rate equal to v_2/L , thereby transferring energy from the elcap to the coil. When the MOSFET is switched OFF, a current will instead flow through the upper MOSFET's body diode and the upper elcap, thus delivering energy to the
10 upper elcap. As shown in figure 2c, the current in the coil will now decrease at a rate v_1/L . As the rate of decrease is lower than the rate of increase (as the voltage v_1 in this case is lower than v_2), it can be advantageous to have the ON-period of the oscillation signal be slightly
15 shorter than the OFF-period, in order to allow for the coil flux to fully be transferred to the upper elcap before the next oscillation starts (discontinuous mode). If the ON- and OFF-periods are equal in length (continuous mode), this may lead to an accumulation of flux, in which case the ferrite core of the coil may saturate. Continuous mode may be advantageous for applications with low voltages and high currents.

Through repeated switching of the lower MOSFET, energy is transferred from the lower elcap to the upper, until, at a point t_2 in time, the lower output voltage v_2 has fallen 20 under a predetermined value, $v_{\text{thresh,OFF}}$, in the example given above at 28 V. With a switching frequency of around 100 kHz, and a coil inductance of around 50 μH , it will require around 500-1000 switching cycles to reduce the output voltage difference by 1 V.

Energy transfer from the upper elcap to the lower is performed in exactly the same way.

25 The person skilled in the art realizes that the present invention by no means is limited to the preferred embodiments described above. On the contrary, many modifications and variations are possible within the scope of the appended claims. For example, the simplified circuit in figure 1 may in reality comprise additional components, that have been omitted for sake of clarity.

CLAIMS:

1. A switch mode power supply for feeding a single ended class D amplifier, said power supply comprising two voltage sources (1, 2), the negative side of the first source (1) being connected to the positive side of the second source (2) in a common point (3), characterized by a voltage equalizer circuit including

5 a coil (10) having a first terminal connected to said common point (3),
a first switch (11) connected between a second terminal of the coil (10) and the positive side of the first source (1),
a second switch (12) connected between said second terminal of the coil (1) and the negative side of the second source (2), and

10 control circuitry (13, 14) for determining when the voltage (v_1, v_2) of one of the voltage sources (1 or 2) exceeds a first threshold value ($v_{\text{thresh,ON}}$), and, in response to such a determination, periodically operate the switch (11 or 12) associated with said one voltage source ON and OFF, thereby redistributing energy from said one voltage source (1 or 2) to the other voltage source (2 or 1).

15 2. A switch mode power supply according to claim 1, wherein the control circuitry comprises one control circuit (13, 14) for each switch (11, 12), each circuit including

20 a comparator (15, 16) connected in parallel with the corresponding voltage source (1, 2) for generating an activation signal (19, 20) while the source output voltage (v_1, v_2) exceeds said first threshold value ($v_{\text{thresh,ON}}$), and
an oscillator (17, 18) connected to the gate of the switch (11, 12) and receiving said activation signal (19, 20), for providing a periodic gate signal (21, 22) when said activation signal (19, 20) is present.

25 3. A switch mode power supply according to claim 2, wherein the comparator further is a hysteresis comparator, adapted to inhibit the activation signal when the source output voltage (v_1, v_2) falls below a second threshold value ($v_{\text{thresh,OFF}}$).

4. A switch mode power supply according to claim 3, wherein said first threshold value ($V_{thresh,ON}$) is greater than said second threshold value ($V_{thresh,OFF}$).

5. A switch mode power supply according to claim 3 or 4, wherein said first and second threshold values ($V_{thresh,ON}$, $V_{thresh,OFF}$) are greater than the desired source output voltage.

6. A switch mode power supply according to any one of the preceding claims, wherein said switches are MOSFET switches (11, 12).

10 7. A switch mode power supply according to any one of the preceding claims, wherein said voltage sources are electrolytic capacitors (1, 2).

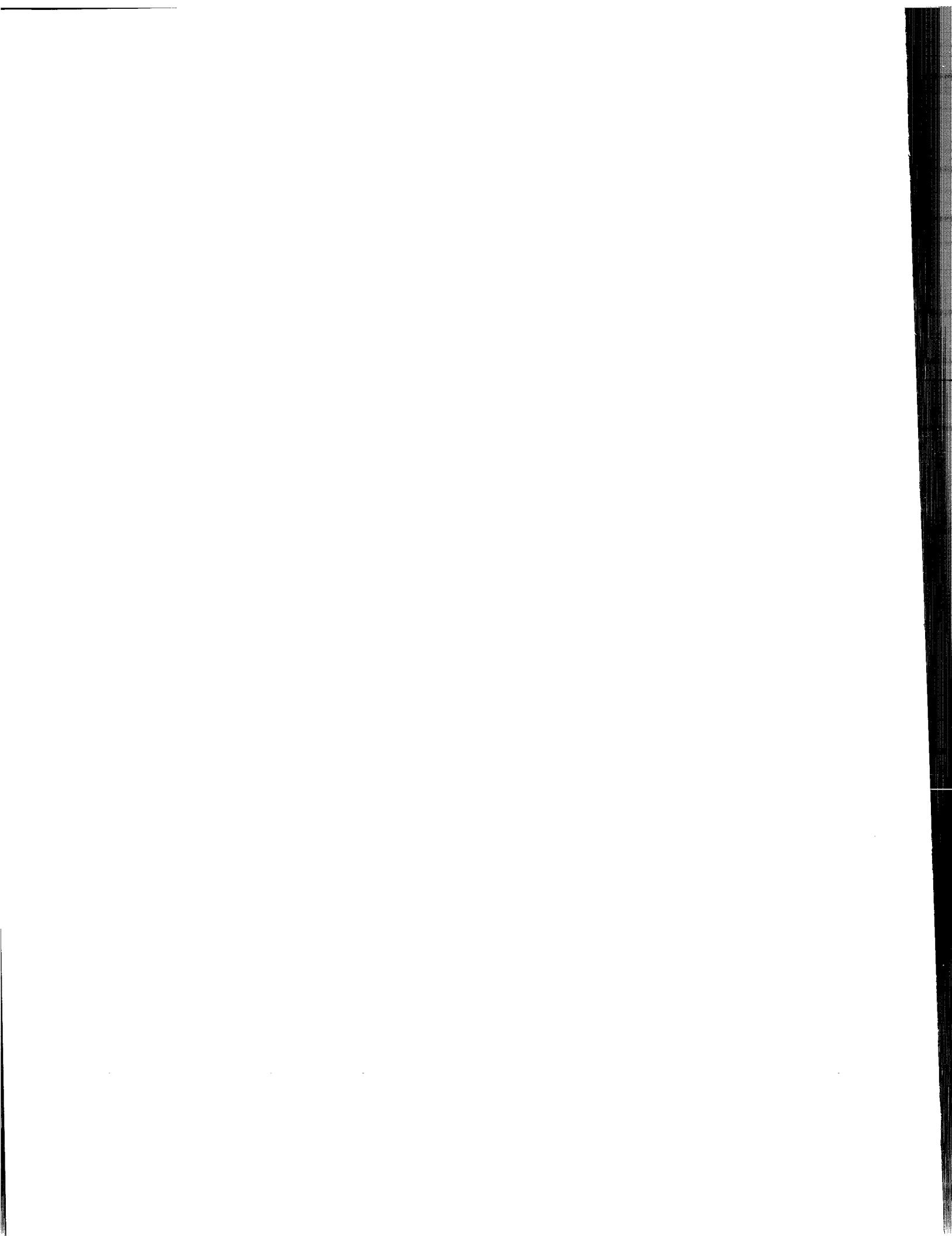
15 8. A class D amplifier fed by a switch mode power supply according to any one of the preceding claims.

ABSTRACT:

A switch mode power supply for feeding a single ended class D amplifier, having two voltage sources (1, 2) is disclosed. The power supply comprises a voltage equalizer circuit including a coil (10) having a first terminal connected to said common point (3), a first switch (11) connected between a second terminal of the coil (10) and the positive side of the first source (1), a second switch (12) connected between said second terminal of the coil (1) and the negative side of the second source (2), and control circuitry (13, 14) for determining when the voltage (v_1, v_2) of one of the voltage sources (1 or 2) exceeds a predetermined threshold value ($v_{thresh,ON}$), and, in response to such a determination, periodically operate the switch (11 or 12) associated with said one voltage source ON and OFF, thereby redistributing energy from said one voltage source (1 or 2) to the other voltage source (2 or 1).

The inventive voltage equalizer thus acts as a switch mode converter, redistributing energy from one voltage source to the other, thereby ensuring that the output voltages of both sources are essentially equal. Further, the voltage equalizer requires very few components, and is thus simple to implement. As the two switches only need to handle the excess of power they can be relatively small and cost efficient.

Fig. 1



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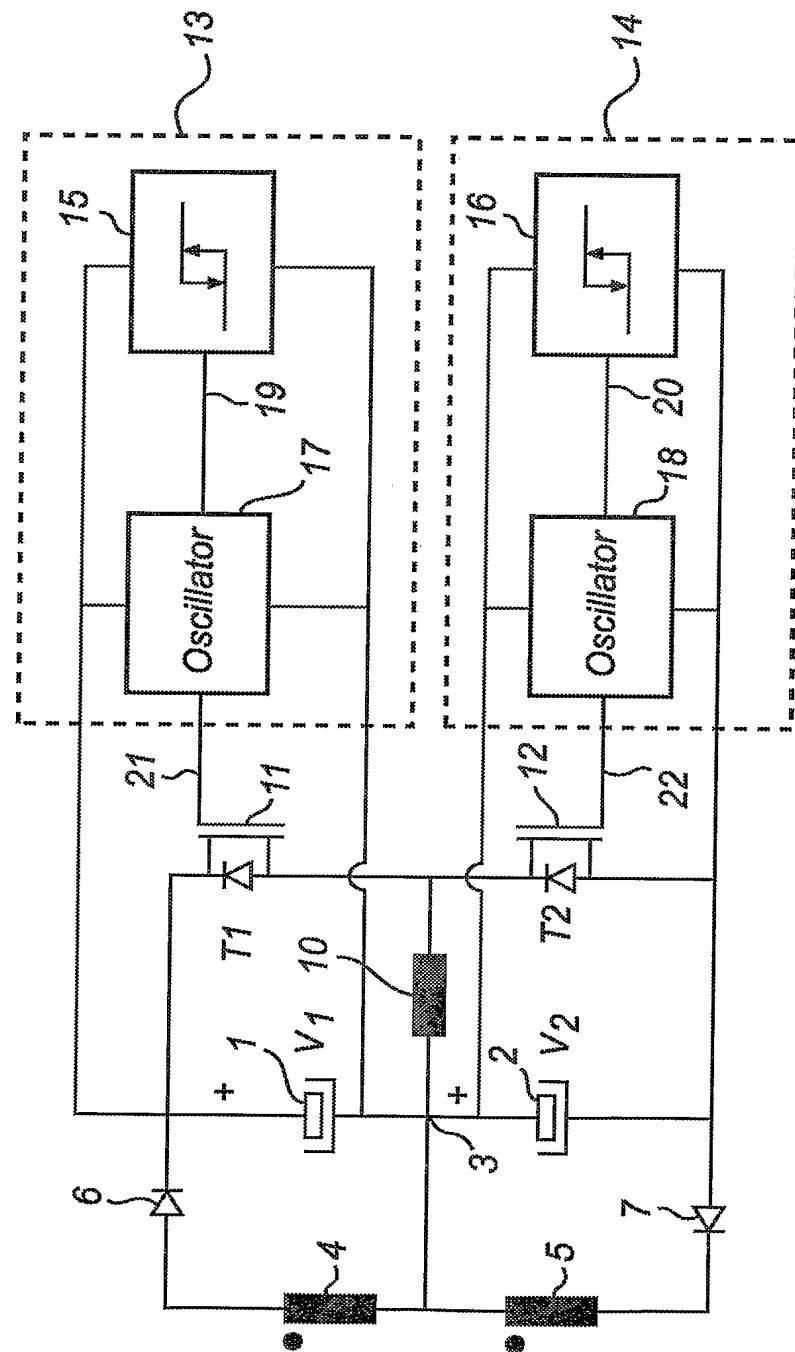


Fig. 1

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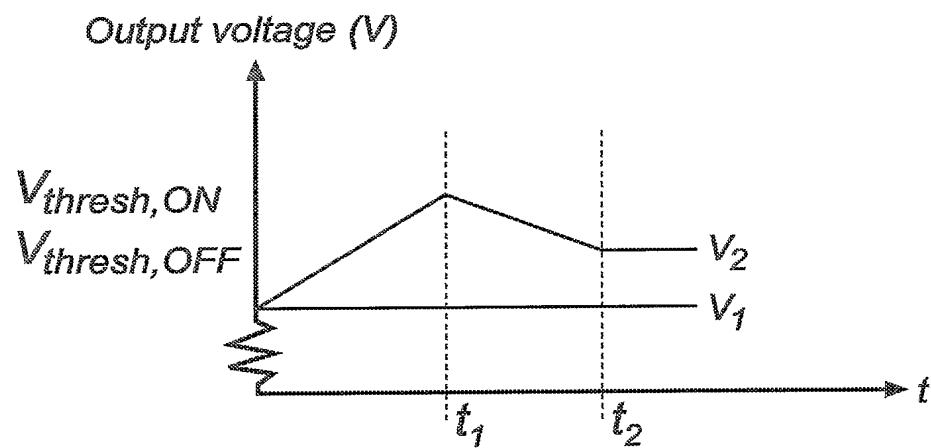


Fig. 2a

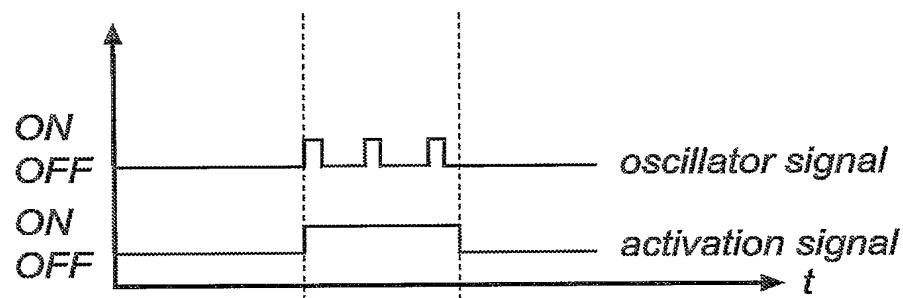


Fig. 2b

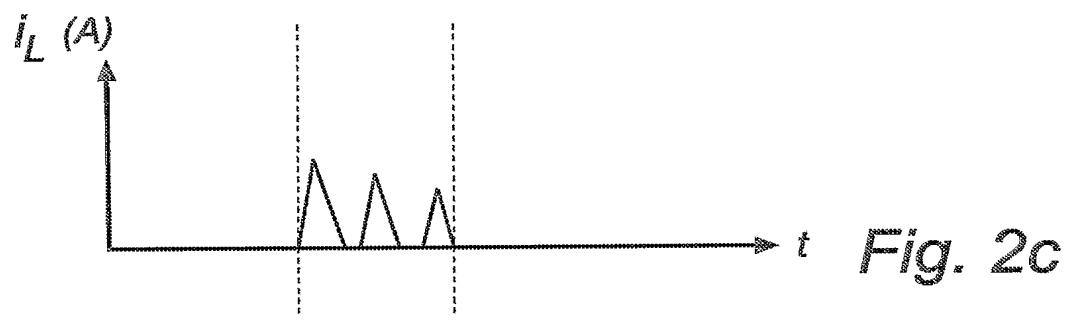
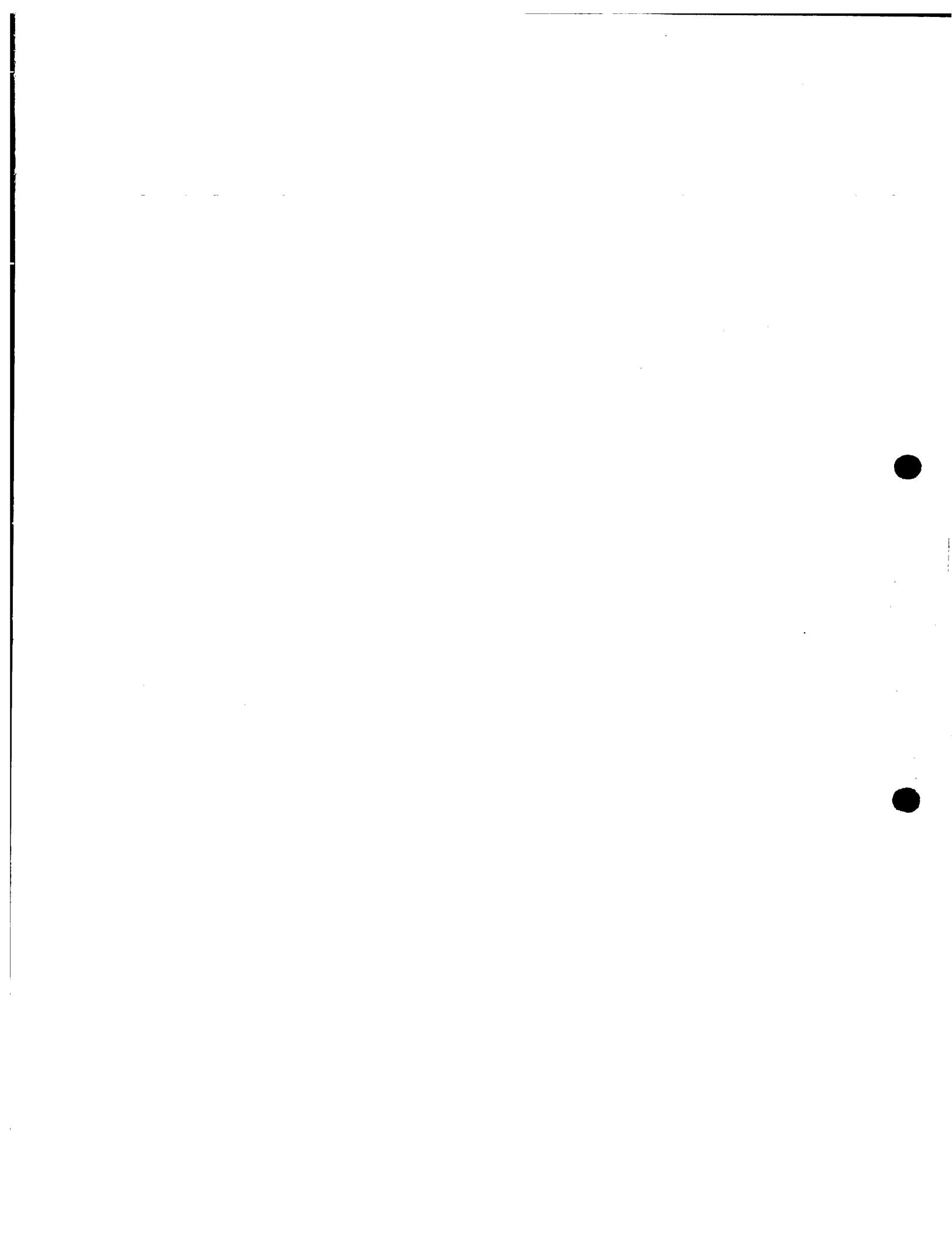


Fig. 2c



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